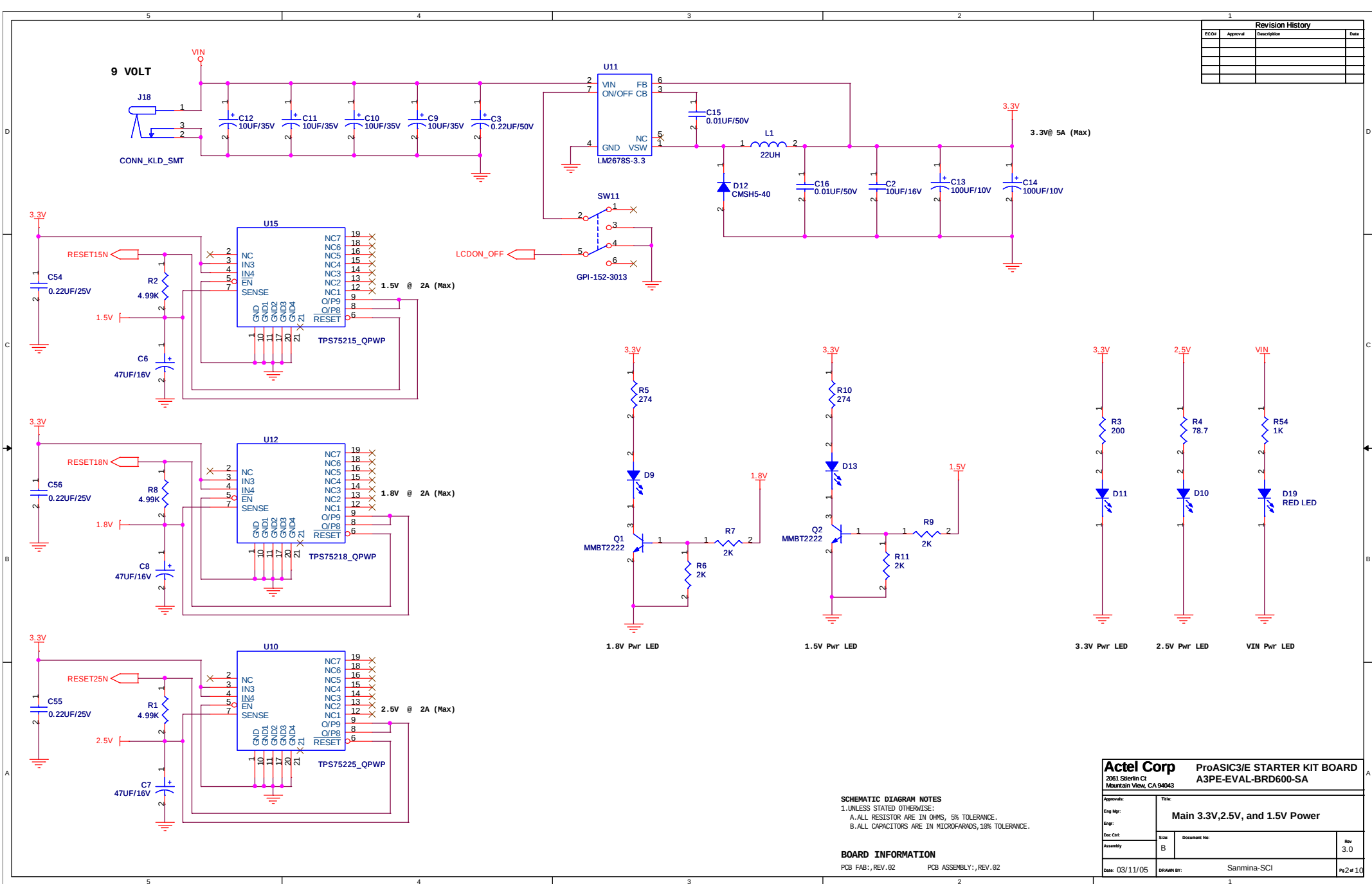


Revision History					
Date	ECO#	Description	Sheet	Initiater	Approval
08/11/04	0.3	Capture the full schematic	1 to 10	Actel	
08/13/04	0.4	Added the LCD MODULE INTERFACE CIRCUIT LCD POWER SUPPLY CIRCUIT LVDS SIGNAL ROUTING VIA CAT-5E CONNECTORS	3,9,10	Sanmina-SCI	
08/16/04	0.5	Schematic changed on	10,5	Sanmina-SCI	
08/20/04	0.6	Changed the LCD MODULE INTERFACE CIRCUIT AND LVDS SIGNAL ROUTING VIA CAT-5E CONNECTORS	3,4	Sanmina-SCI	
08/26/04	0.7	Schematic changed on	3,4,6	Sanmina-SCI	
08/27/04	0.8	Schematic changed on	2,3,6,7,10	Sanmina-SCI	
08/28/04	0.9	Schematic changed on	2,8	Sanmina-SCI	
09/03/04	1.0	Schematic changed on	2,3,4,5,6,7,8,9,10	Sanmina-SCI	
09/04/04	1.1	Schematic changed on	6,8	Sanmina-SCI	
09/06/04	1.2	Schematic changed on	1,2,3,4,5,6,7,8,9,10	Sanmina-SCI	
09/07/04	1.3	Schematic changed on	2,4,6,7,8	Sanmina-SCI	
09/07/04	1.4	Schematic changed on	10	Sanmina-SCI	
09/08/04	1.5	Schematic changed on	1,2,10	Sanmina-SCI	
09/09/04	1.6	Schematic changed on	6,8	Sanmina-SCI	
09/20/04	1.7	Bom Changed on	1,2,3,4,5,6,7,8,9,10	Sanmina-SCI	
09/24/04	1.8	Schematic changed on	5	Sanmina-SCI	
02/18/05	2.0	Schematic changed on	3,4,6,7	Sanmina-SCI	
02/22/05	2.1	Schematic changed on	2,4,6,7,10	Sanmina-SCI	
02/24/05	2.2	Schematic changed on	1,2,3,4,5,6,7,8,9,10	Sanmina-SCI	
03/07/05	2.3	Schematic changed on	3,5,6,7,10	Sanmina-SCI	
03/10/05	2.4	Schematic changed on	7,10	Sanmina-SCI	
04/05/05	3.0	Schematic changed on	8	Sanmina-SCI	

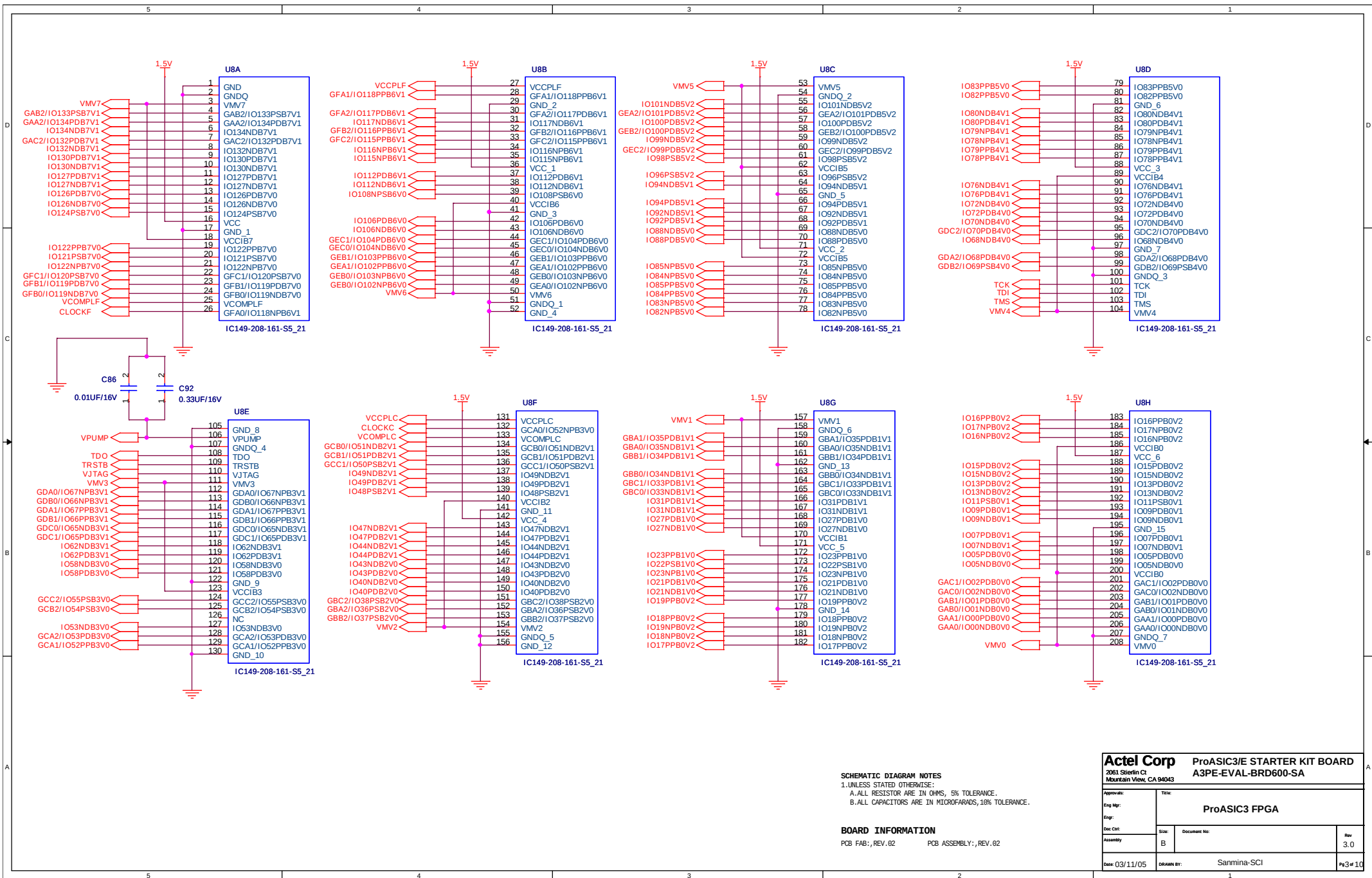
ProASIC3/E STARTER KIT BOARD

Actel Corp 20655 Stearns Ct Mountain View, CA 94039		ProASIC3/E STARTER KIT BOARD A3PE-EVAL-BRD600-SA	
Part No:	Rev:	ProASIC3/E STARTER KIT BOARD	
Part Desc:	Rev:	Document No:	Rev:
Manufacturer:		Sanmina-SCI	3.0
Date: 03/11/05	Revised By:	Pg 1 of 10	

Revision History			
ECOF	Approval	Description	Date



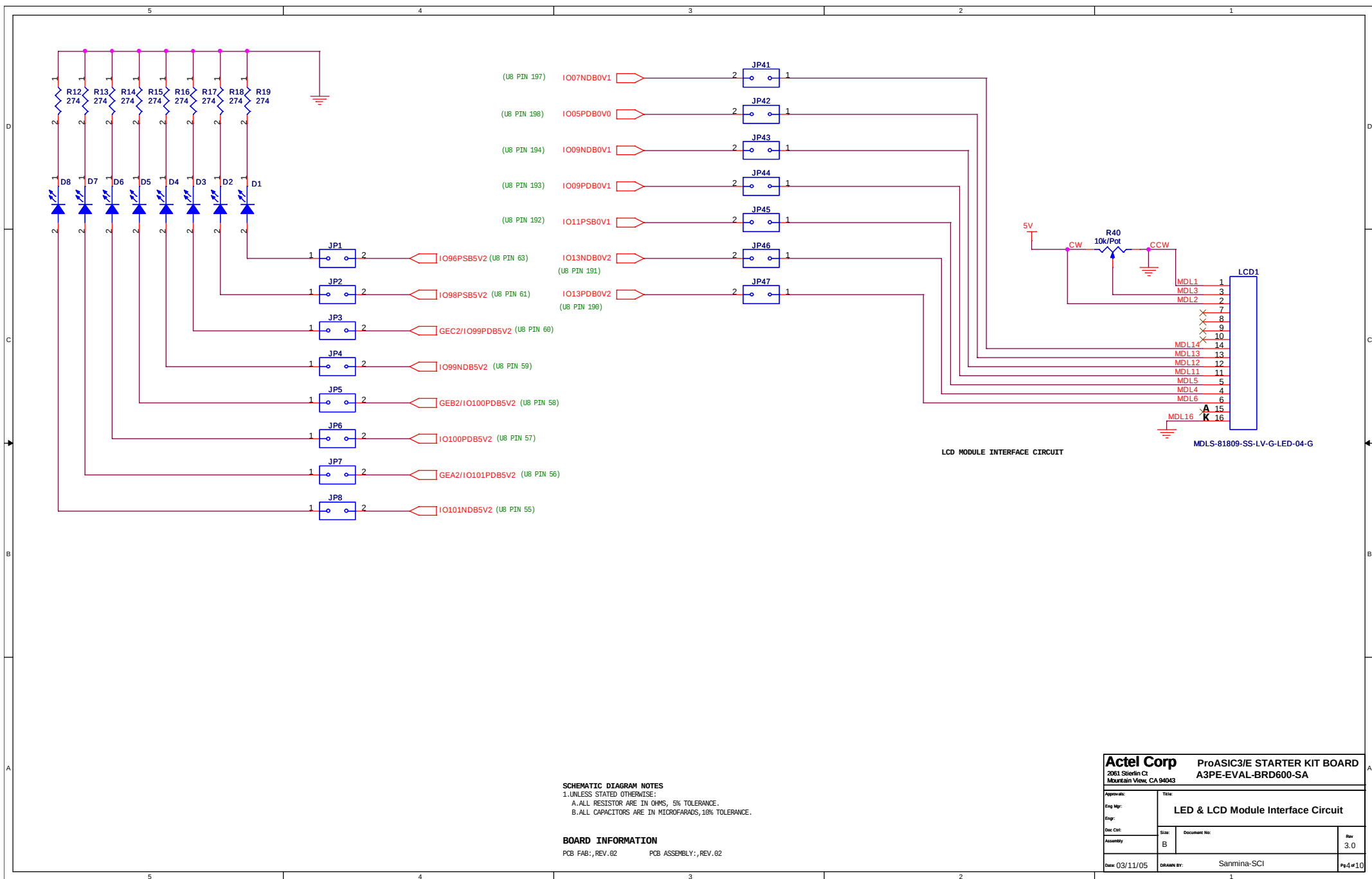
Actel Corp		ProASIC3/E STARTER KIT BOARD	
2061 Sterlin Ct Mountain View, CA 94043		A3PE-EVAL-BRD600-SA	
Approved:		Title:	
Eng Mgr:		Main 3.3V, 2.5V, and 1.5V Power	
Eng:			
Doc Crt:			
Assembly:			
Size:		Document No:	Rev
B			3.0
Date: 03/11/05		DRAWN BY: Sanmina-SCI	
		Pg 2 of 10	

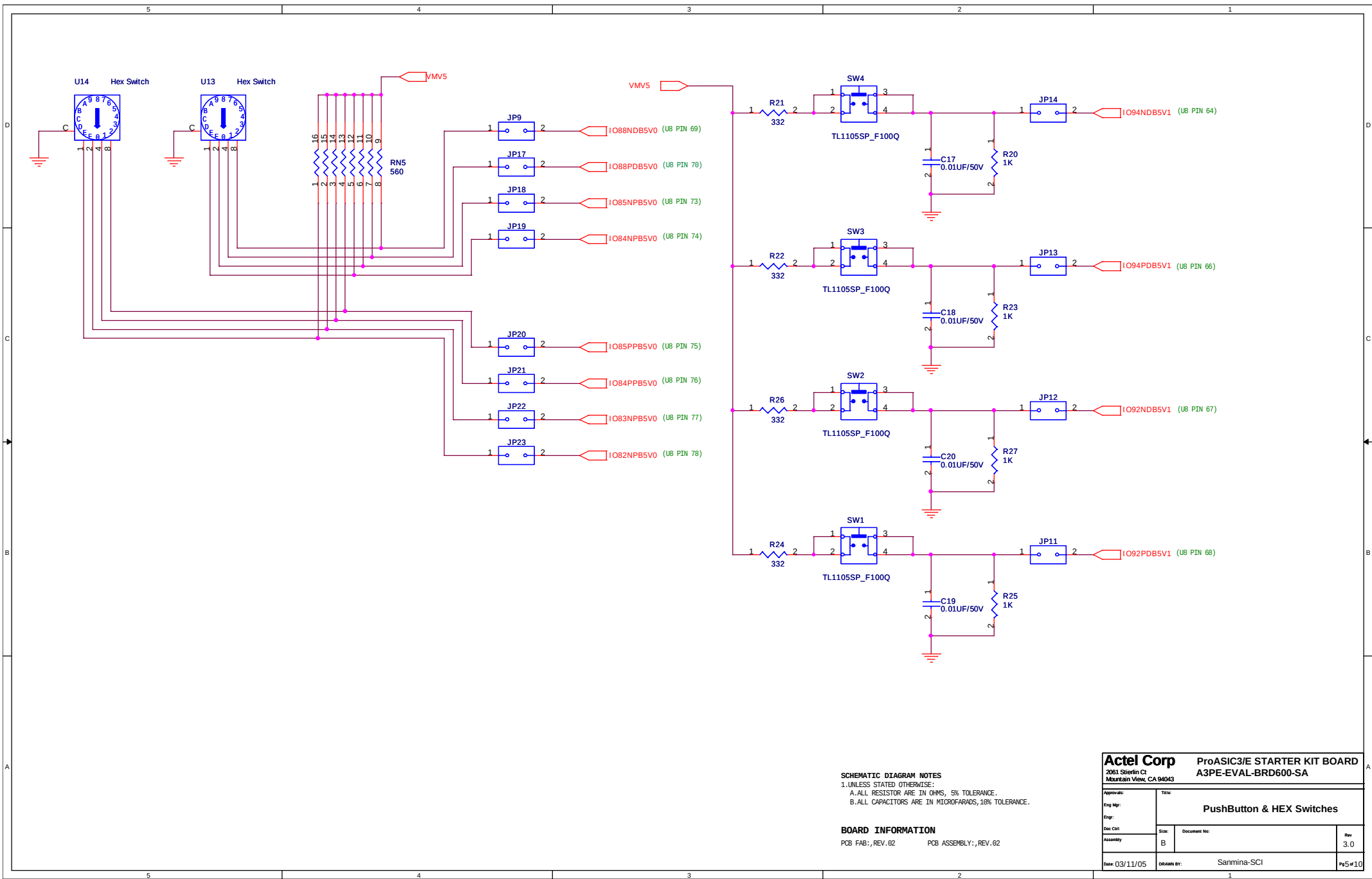


SCHEMATIC DIAGRAM NOTES
1. UNLESS STATED OTHERWISE:
A. ALL RESISTOR ARE IN OHMS, 5% TOLERANCE.
B. ALL CAPACITORS ARE IN MICROFARADS, 10% TOLERANCE.

BOARD INFORMATION
PCB FAB: ,REV.02 PCB ASSEMBLY: ,REV.02

Actel Corp 2061 Shierlin Ct Mountain View, CA 94043		ProASIC3/E STARTER KIT BOARD A3PE-EVAL-BRD600-SA	
Approved:		Title:	
Eng Mgr:		ProASIC3 FPGA	
Doc Cnt:		Size:	Document No:
Assembly:		B	Rev 3.0
Date: 03/11/05		DRAWN BY: Sammina-SCI	
		pg 3 of 10	





Actel Corp		ProASIC3/E STARTER KIT BOARD	
2061 Shiloh Ct Mountain View, CA 94043		A3PE-EVAL-BRD600-SA	
Title: PushButton & HEX Switches			
Approved:	Eng Mgr:	Size: B	Document No:
Doc Cnt:	Assembly:		Rev 3.0
Date: 03/11/05	Drawn By: Sammina-SCI	Pg 5 of 10	

