

### Lab3 Post Lab Questions

1. (2 pts ea) For the equation below:

$$C_{new} = C_a * F + C_b * (1 - F)$$

Let  $C_a = 0.75$ ,  $C_b = 0.625$ ,  $F = 0.325$ . Give the 8 bit binary values for:

- a.  $C_a =$  \_\_\_\_\_
- b.  $C_b =$  \_\_\_\_\_
- c.  $F =$  \_\_\_\_\_
- d.  $1 - F =$  \_\_\_\_\_ (as calculated in this lab)
- e.  $C_a * F =$  \_\_\_\_\_ (upper 8 bits only)
- f.  $C_b * (1 - F) =$  \_\_\_\_\_ (upper 8 bits only)
- g.  $C_{new} =$  \_\_\_\_\_ (8 bits)
- h.  $C_{new}$  \_\_\_\_\_ (decimal value)

2. (4 pts each) Remember that our calculation of  $C_{new}$  is only an approximation. What is the correct value of  $C_{new}$  without any approximations?

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What is the percent error between the correct value of  $C_{new}$  and the approximate value of  $C_{new}$  computed by the hardware ?

(compute as  $(C_{new\_correct} - C_{new\_approximate}) / C_{new\_correct} * 100 \%$ )

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How large is the absolute error in terms of Least Significant Bits ? (1 LSB =  $1/256$ ). Compute as  $(C_{new\_correct} - C_{new\_approximate}) / (1/256)$ .

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3. (5 pts) What is the functionality of the Carry Chain in the Altera Flex 10K FPGA? What is the advantage of using carry chain logic over just using normal LUTs?