Figure 5

(a)



I/O PADS ILA ILA IΡ **IBA CUSTOM** CORE LOGIC EMBEDDED SYSTEM **PADS** I/O PADS **BUS** CUSTOM PPC405 9 ILA CORE CORE **NOTES:** ILA:INTEGRATED LOGIC ANALYZER. **MEMORY** IBA: INTEGRATED BUS ANALYZER. **ARRAY** ILA ICON: INTEGRATED CONTROLLER. **ICON** BOUNDARY SCAN TAP CONTROLLER (b) TARGET CONNECTION

Some embedded-logic analyzers employ devices' built-in logic circuits and routing resources (a, courtesy Actel); others take the form of a "soft" core (b, courtesy Xilinx).